A picture containing text, electronics, computer, display

Description automatically generated

Switches are first set to 10’b 00 0000 0000

Switches are set to 10’b 00 0000 0010

(2 in decimal)

Switches are set to 10’b 00 0000 0111

(7 in decimal)

Run is triggered to start the Basic I/O Test 1 program

HEX0 is displaying the SW value 16’h2 in real time

HEX0 is displaying the SW value 16’h7 in real time

Switches are set to 10’b 00 0000 0011

(16’h0003) to jump to the start address x0003 of the Basic I/O Test 1 program

MAR = 16’hFFFF & Writing

Store value in internal register and display instead of storing to M[xFFFF]

MAR = 16’hFFFF & Reading

Load SW value instead of M[xFFFF]

Reset the entire SLC-3 processor

All registers are cleared

FSM is set to Halted state